

AN SRAM DEVICE AND A METHOD OF OPERATING THE SAME
TO REDUCE LEAKAGE CURRENT DURING A SLEEP MODE

ABSTRACT OF THE DISCLOSURE

An SRAM device and a method of operating an SRAM device. In one embodiment, the SRAM device includes (1) an SRAM array coupled to row peripheral circuitry by a word line and coupled to column peripheral circuitry by bit lines and (2) a sleep mode voltage controller configured to provide both an array high supply voltage V_{ADD} that is lower than a high operating voltage V_{DD} and an array low supply voltage V_{ASS} that is higher than a low operating voltage V_{SS} to the SRAM array during a sleep mode.